

WHAT IS CLAIMED:

1. A clock system comprising:

5 a receiver circuit for receiving a binary coded time signal, comprising:

10 a microprocessor clock unit connected to said receiver circuit and programmed to energize said receiver circuit for a minimum time period necessary to receive said binary coded time signal, and to shut said receiver circuit off after said minimum time period; and

a clock display connected to said microprocessor clock unit for displaying time.

2. The clock system as recited in claim 1 wherein said minimum time period necessary to receive said binary coded time signal is sufficient to insure receipt of a full one minute time signal within said minimum time period.

3. The clock system as recited in claim 1 wherein said minimum time period necessary to receive said binary coded time signal is sufficient to insure receipt of a small portion of a full one minute time signal necessary to provide a time update having a magnitude of no more than five seconds.

4. The clock system as recited in claim 1 wherein said microprocessor clock unit includes programming for a separate first time storage and a separate second time storage and retrieval to enable a user to energize said receiver circuit for
5 a minimum time period necessary to receive said binary coded time signal in said first time storage and without disrupting said second time storage.

5. The clock system as recited in claim 4 wherein said separate first time storage and said separate second time storage are each associated with a separate binary coded time signal.

6. The clock system as recited in claim 4 wherein said first said time storage is not disrupted in absense of said binary coded time signal.